Sar Adc Matlab Code

Real-Time Digital Signal ProcessingLow-Power High-Speed ADCs for Nanometer CMOS IntegrationApplied Intelligent Control of Induction Motor DrivesSystematic Design for Optimisation of Pipelined ADCsDigital Signal ProcessingAn Introduction to Mixed-signal IC Test and MeasurementDesign, Implementation and Testing of a Hybrid Algorithmic [sigma Delta] A/D ConverterSimulation and Analysis of Modern Power SystemsEnergy Scalable Analog-to-digital Conversion Using Passive Sigma-delta ModulatorsProceedings of the ... IEEE Instrumentation and Measurement Technology ConferenceProceedings2003 IEEE Nuclear Science SymposiumCERN.IEEE Instrumentation and Measurement Technology Conference ProceedingsThe ... IEEE Asia Pacific Conference on ASICsDesign of a Passive Sonar System for the Cornell University Autonomous Underwater Vehicle ProjectSpacecraft SensorsIEEE Transactions on Circuits and SystemsIEEE International Geoscience and Remote Sensing Symposium ProceedingsJournal of the Korean Physical Society Sen M. Kuo Zhiheng Cao Tze Fun Chan João Goes Li Tan Gordon W. Roberts Cheong Kun Ranjana Sodhi Jeffrey Loo Institut de recherches subatomiques de Strasbourg Rives W. Borland Mohamed M Abid Real-Time Digital Signal Processing Low-Power High-Speed ADCs for Nanometer CMOS Integration Applied Intelligent Control of Induction Motor Drives Systematic Design for Optimisation of Pipelined ADCs Digital Signal Processing An Introduction to Mixed-signal IC Test and Measurement Design, Implementation and Testing of a Hybrid Algorithmic [sigma Delta] A/D Converter Simulation and Analysis of Modern Power Systems Energy Scalable Analog-to-digital Conversion Using Passive Sigma-delta Modulators Proceedings of the ... IEEE Instrumentation and Measurement Technology Conference Proceedings 2003 IEEE Nuclear Science Symposium CERN. IEEE Instrumentation and

Measurement Technology Conference Proceedings The ... IEEE Asia Pacific Conference on ASICs Design of a Passive Sonar System for the Cornell University Autonomous Underwater Vehicle Project Spacecraft Sensors IEEE Transactions on Circuits and Systems IEEE International Geoscience and Remote Sensing Symposium Proceedings Journal of the Korean Physical Society *Sen M. Kuo Zhiheng Cao Tze Fun Chan João Goes Li Tan Gordon W. Roberts Cheong Kun Ranjana Sodhi Jeffrey Loo Institut de recherches subatomiques de Strasbourg Rives W. Borland Mohamed M Abid*

real time digital signal processing implementations and applications has been completely updated and revised for the 2nd edition and remains the only book on dsp to provide an overview of dsp theory and programming with hands on experiments using matlab c and the newest fixed point processors from texas instruments ti

low power high speed adcs for nanometer cmos integration is about the design and implementation of adc in nanometer cmos processes that achieve lower power consumption for a given speed and resolution than previous designs through architectural and circuit innovations that take advantage of unique features of nanometer cmos processes a phase lock loop pll clock multiplier has also been designed using new circuit techniques and successfully tested 1 a 1 2v 52mw 210ms s 10 bit two step adc in 130nm cmos occupying 0 38mm2 using offset canceling comparators and capacitor networks implemented with small value interconnect capacitors to replace resistor ladder multiplexer in conventional sub ranging adcs it achieves 74db sfdr for 10mhz and 71db sfdr for 100mhz input 2 a 32mw 1 25gs s 6 bit adc with 2 5ghz internal clock in 130nm cmos a new type of architecture that combines flash and sar enables the lowest power consumption 6 bit 1gs s adc reported to date this design can be a drop in replacement for existing flash adcs since it does require any post processing or calibration step and has the same latency as flash 3 a 0 4ps rms jitter integrated from 3khz to 300mhz offset for 2 5ghz 1 3ghz tunable phase noise programmable clock multiplier pll for generating sampling clock to the sar adc a new loop filter structure enables phase error

preamplification to lower pll in band noise without increasing loop filter capacitor size

induction motors are the most important workhorses in industry they are mostly used as constant speed drives when fed from a voltage source of fixed frequency advent of advanced power electronic converters and powerful digital signal processors however has made possible the development of high performance adjustable speed ac motor drives this book aims to explore new areas of induction motor control based on artificial intelligence ai techniques in order to make the controller less sensitive to parameter changes selected ai techniques are applied for different induction motor control strategies the book presents a practical computer simulation model of the induction motor that could be used for studying various induction motor drive operations the control strategies explored include expert system based acceleration control hybrid fuzzy pi two stage control neural network based direct self control and genetic algorithm based extended kalman filter for rotor speed estimation there are also chapters on neural network based parameter estimation genetic algorithm based optimized random pwm strategy and experimental investigations a chapter is provided as a primer for readers to get started with simulation studies on various ai techniques presents major artificial intelligence techniques to induction motor drives uses a practical simulation approach to get interested readers started on drive development authored by experienced scientists with over 20 years of experience in the field provides numerous examples and the latest research results simulation programs available from the book s companion website this book will be invaluable to graduate students and research engineers who specialize in electric motor drives electric vehicles and electric ship propulsion graduate students in intelligent control applied electric motion and energy as well as engineers in industrial electronics automation and electrical transportation will also find this book helpful simulation materials available for download at wiley com go chanmotor

systematic design for optimisation of pipelined adcs proposes and develops new strategies methodologies and tools for designing low power and low area cmos pipelined

a d converters the task is tackled by following a scientifically consistent approach first of all the state of the art in pipeline a d converters is analysed with a double purpose a to identify the best suited among different strategies reported in literature and taking into account the objectives pursued b to identify the drawbacks of these strategies as a basic first step to improve them then the book proposes a top down design approach for implementing high performance low power and low area cmos pipelined a d converters through the conception development and implementation of self calibrated techniques to extend the linearity of some critical stages in the architecture of pipelined adcs the detailed analysis and modelling of some major non idealities that limit the physical realisation of pipelined adcs and the proposal development and implementation of design methodologies to support systematic design of optimised instances of these converters which combine maximum performance with minimum power dissipation and minimum area occupation list several implementations together with consistent measured results are presented in particular a practical realisation of a low power 14 bit 5ms s cmos pipelined adc with background analogue self calibration is fully described the proposed approach is fully in line with the best practice regarding the design of mixed signal integrated circuits on the one hand drawbacks of currently existing solutions are overcame through innovative strategies and on the other hand the expert knowledge is packaged and made available for re usability by the community of circuit designers finally feasibility of the strategies and the associated encapsulated knowledge isgranted through experimental validation of working silicon systematic design for optimisation of pipelined adcs serves as an excellent reference for analogue design engineers especially designers of low power cmos a d converters the book may also be used as a text for advanced reading on the subject

digital signal processing fundamentals and applications third edition not only introduces students to the fundamental principles of dsp it also provides a working knowledge that they take with them into their engineering careers many instructive worked examples are

used to illustrate the material and the use of mathematics is minimized for an easier grasp of concepts as such this title is also useful as a reference for non engineering students and practicing engineers the book goes beyond dsp theory showing the implementation of algorithms in hardware and software additional topics covered include adaptive filtering with noise reduction and echo cancellations speech compression signal sampling digital filter realizations filter design multimedia applications over sampling etc more advanced topics are also covered such as adaptive filters speech compression such as pcm μ law adpcm and multi rate dsp over sampling adc subband coding and wavelet transform covers dsp principles with an emphasis on communications and control applications includes chapter objectives worked examples and end of chapter exercises that aid the reader in grasping key concepts and solving related problems provides an accompanying website with matlab programs for simulation and c programs for real time dsp presents new problems of varying types and difficulties

with the proliferation of complex semiconductor devices containing digital analog mixed signal and radio frequency circuits the economics of test has come to the forefront and today s engineer needs to be fluent in all four circuit types having access to a book that covers these topics will help the evolving test engineer immensely and will be an invaluable resource in addition the second edition includes lengthy discussion on rf circuits high speed i os and probabilistic reasoning appropriate for the junior senior university level this textbook includes hundreds of examples exercises and problems

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spacecraft sensors the first of its kind offers a comprehensive review of many aspects and intricacies of sensors used in the spacecraft industry it covers sensor development from concept design and cost to building testing interfacing integrating and on orbit operation it is intended for the specialist or non specialist engineer scientist and those involved in the business aspect of the spacecraft industry focusing on how these various disciplines contribute to the development of a sensor used in space this key text explains how mathematics physics business and engineering based concepts are used to develop and design a sensor which complies with a set of specific requirements discusses essential topics such as cost estimation signal processing noise reduction filters phased arrays radars optics and radiometers used in space operation covers a range of typical sensors used in the spacecraft industry such as infrared passive microwave radars and spacebased gps sensors concludes each chapter with examples of past and current orbiting sensors such as dsp sbirs champ landsat and goes to illustrate how concepts are applied includes the matlab codes used to create the example plots in order to give the reader a starting point for further analysis spacecraft sensors is an invaluable resource for engineers technical consultants those in the business division and research scientists associated with spacecraft projects it is also an excellent textbook for undergraduate and postgraduate students studying the development design and

applications of spacebased sensors

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