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processor and program the risc v processor in hardware simulation software simulation and in hardware includes both systemverilog and vhdl designs of fundamental building blocks as well as of single cycle multicycle and pipelined versions of the risc v architecture features a companion website with a bonus chapter on i o systems with practical examples that show how to use sparkfun s red v redboard to communicate with peripheral devices such as lcds bluetooth radios and motors the companion website also includes appendices covering practical digital design issues and c programming as well as links to cad tools lecture slides laboratory projects and solutions to exercises see the companion edx moocs engr85a and engr85b with video lectures and interactive problems

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